

B.Tech. SEM -VI Info. Tech. 2014 Course (CBCS) : WINTER - 2018

SUBJECT: COMPUTER ORGANIZATION & ARCHITECTURE

Day: Friday  
Date: 16/11/2018

W-2018-2486

Time: 10.00 AM TO 01.00 PM  
Max Marks: 60

N.B.:

- 1) All questions are **COMPULSORY**.
- 2) Figures to the right indicate **FULL** marks.
- 3) Assume suitable data, if necessary.
- 4) Use of non programmable calculator is **ALLOWED**.
- 5) Draw neat and labeled diagrams **WHEREVER** necessary.

- Q.1 Write a note on segmentation of 80386DX and compare it with 8086. (10)  
**OR**
- Q.1 Describe 80386DX descriptor tables also explain code, data and stack descriptors. (10)
- Q.2 Explain flash memory, SDRAM and PRAM in detail. (10)  
**OR**
- Q.2 Describe segmentation and paging in 80386DX in detail. (10)
- Q.3 Explain multiplier control unit with neat diagram. (10)  
**OR**
- Q.3 What is microprogram? Write applications of microprogramming. (10)
- Q.4 Explain addressing modes of Intel Pentium processor. What are the features of Pentium processor? (10)  
**OR**
- Q.4 Describe features of superscalar architecture. (10)
- Q.5 Write detailed note on SPARC architecture. (10)  
**OR**
- Q.5 What is RISC? Explain RISC Processor architecture. (10)
- Q.6 Write detailed note on Bernstein conditions of parallelism. (10)  
**OR**
- Q.6 Explain architectural classification of parallel processors. (10)