

B-Tech-sem-V, (2014 course), IT
winter-2018

B.Tech. SEM -V Info. Tech. 2014 Course (CBCS) : WINTER - 2018

SUBJECT: MICROPROCESSOR ARCHITECTURE AND PROGRAMMING

Day : Thursday
Date : 29/11/2018

W-2018-2415

Time: 02.30 PM TO 05.30 PM
Max. Marks: 60

N.B.:

- 1) All questions are **COMPULSORY**.
- 2) Figures to the right indicate **FULL** marks.
- 3) Assume suitable data if necessary.

Q.1 Explain the 8086 processor architecture with neat diagram. **(10)**

OR

Draw and explain the minimum mode configuration of 8086 processor.

Q.2 Explain the programmer's model of 8086 processor. **(10)**

OR

Explain the following assembler directives:

- a) MACRO b) ENDP c) ASSUME

Q.3 Draw and explain the 8255 PPI architecture. **(10)**

OR

Explain how the communication takes place between 8086 processor and 8087 NDP.

Q.4 Explain the closely coupled configuration of multiprocessor organization with suitable diagram. **(10)**

OR

Explain the superscalar architecture with neat diagram.

Q.5 Explain the architecture of 8051 microcontroller. **(10)**

OR

Explain the programmer's model of 8051 microcontroller.

Q.6 Explain the interrupt structure of 8051 microcontroller. **(10)**

OR

Explain the serial port programming of 8051 microcontroller.

* * * * *