

B.Tech. SEM -VI Info. Tech. 2014 Course (CBCS) : SUMMER - 2019
SUBJECT: COMPUTER ORGANIZATION & ARCHITECTURE

Day: Wednesday
Date: 29/05/2019

Time; 02.30 PM TO 05.30 PM
Max Marks: 60

S-2019-2751

N.B.:

- 1) All questions are **COMPULSORY**.
- 2) Figures to the right indicate **FULL** marks.
- 3) Assume suitable data, if necessary.
- 4) Use of non programmable calculator is **ALLOWED**.
- 5) Draw neat and labeled diagrams **WHEREVER** necessary.

Q.1 Draw functional block diagram of 80386DX. Explain features of 80386DX. **(10)**

OR

Q.1 Write difference between three operating modes of 80386DX. Explain any one in detail. **(10)**

Q.2 Describe UMA, NUMA and COMA with neat diagram. **(10)**

OR

Q.2 What is secondary storage? Explain RAID, Blue ray Disk, SSD and cloud storage in brief. **(10)**

Q.3 Explain sequence counter hardwired control design method in detail. **(10)**

OR

Q.3 Explain applications of microprogramming in detail. **(10)**

Q.4 What are the features of Intel Pentium processor? Draw and explain block diagram. **(10)**

OR

Q.4 Describe cache coherence in Pentium processor. **(10)**

Q.5 Explain closely coupled and loosely coupled multiprocessor systems. **(10)**

OR

Q.5 Explain Intel core i3, i5 and i7 architectures. **(10)**

Q.6 What is instruction pipelining? Explain pipelining in 80386DX Processor. **(10)**

OR

Q.6 Explain Flynn's classification of parallel processing. **(10)**