

B.Tech. SEM -V Info. Tech. 2014 Course (CBCS) : SUMMER - 2019
SUBJECT: MICROPROCESSOR ARCHITECTURE AND PROGRAMMING

Day : Tuesday
Date : 14/05/2019

S-2019-2680

Time: 10.00 AM TO 01.00 PM
Max. Marks: 60

N.B.:

- 1) All questions are **COMPULSORY**.
- 2) Figures to the right indicate **FULL** marks.
- 3) Assume suitable data if necessary.

Q.1 Explain the different types of 8086 interrupts. **(10)**

OR

Draw and explain the maximum mode configuration of 8086 processor.

Q.2 What are the various addressing modes of 8086 processor? Explain each by suitable example. **(10)**

OR

Explain the following assembler directives:

- a) PROC b) EXTRN c) ASSUME

Q.3 Explain the architecture of 8259 PIC with neat diagram. **(10)**

OR

Explain the master-slave configuration of 8259 PIC.

Q.4 Explain the loosely coupled configuration of multiprocessor organization. **(10)**

OR

Differentiate between RISC and CISC architecture.

Q.5 Explain the details of 8051 microcontroller data memory. **(10)**

OR

Explain the various addressing modes of 8051 microcontroller.

Q.6 Explain the interrupt response of 8051 microcontroller. **(10)**

OR

Explain the various tools used for the development of 8051 based system.

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