

BACHELOR OF TECHNOLOGY (CBCS) (2014 COURSE)
Information Technology
B.Tech.Sem - VI :SUMMER : 2023
SUBJECT : COMPUTER ORGANIZATION & ARCHITECTURE

Day : Tuesday

Date : 30-05-2023

Time : 02:30 PM-05:30 PM

S-13425-2023

Max. Marks : 60

N.B.:

- 1) All questions are **COMPULSORY**.
- 2) Figures to the right indicate **FULL** marks.
- 3) Draw neat diagrams **WHEREVER** necessary.

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- Q.1 Explain memory management in 80386. (10)
- OR**
- Q.1 Explain the real address mode programmer's model of 80386 processor. (10)
- Q.2 Describe the terms SDRAM, DDR3, DDR4 and flash memory. (10)
- OR**
- Q.2 What is segmentation and paging? Describe in detail. (10)
- Q.3 Explain applications of microprogramming in detail. (10)
- OR**
- Q.3 Explain Hardwired control Design methods. (10)
- Q.4 What are the features of Intel Pentium processor? Draw and explain block diagram. (10)
- OR**
- Q.4 How Cache coherence is achieved in Pentium processor? Explain. (10)
- Q.5 Explain Intel core i3, i5 and i7 architecture. (10)
- OR**
- Q.5 Explain the features of CISC architecture with neat diagram. (10)
- Q.6 Explain flynn's classification of parallel processing. (10)
- OR**
- Q.6 Explain the Berstein's condition of detecting parallelism. (10)

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